

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application of: Shunichi SATO et al;

Serial No.: 10/567,809

Date Filed: April 25, 2008



For: SURFACE-EMISSION LASER DIODE AND FABRICATION PROCESS THEREOF

Conf. No.: 6291

Class-Subclass: 372-050124

**Issue Fee Due Date: February 9, 2010**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**APPLICANT'S COMMENTS ON  
EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE**

Sir:

Applicant appreciates the Examiner's Statement of the Reasons for Allowance attached to the Notice of Allowability dated November 9, 2009 and submits that the allowed claims recite subject matter which further supports patentability for reasons in addition to those identified in the Reasons for Allowance.

Respectfully submitted,

A handwritten signature of Paul Teng.

I hereby certify that this correspondence is being deposited this date with the U.S. Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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January 25, 2010  
Date

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